J-TAG Emulator and VisualDSP™ for SHARC

Ordering Information

Model	Description
8555	J-TAG Emulator for PC hosts
8555S	Analog Devices VisualDSP Development Software

Options:

-010	Windows 98/NT/2000

www.pentek.com

PENTEK

Pentek **Model 8555** supports C-language software development utilizing the J-TAG emulator ports on Pentek's Model 4288 Multi-SHARC I/O Processor board.

Model 8555 is for PC development workstations. It includes an ISA circuit board which fits easily into any PC and an emulation pod with a J-TAG cable.

Model 8555S VisualDSP® from Analog Devices is a software development system designed to work with the Model 8555.

VisualDSP introduces an easy to use project management environment comprised of an Integrated Development Environment (IDE) and Debugger. It enables you to manage projects from start to finish from within a single, integrated interface. You can move easily between building, editing and debugging activities.

The IDE includes access to the SHARC DSP C Compiler, C Runtime Library, Linker, Loader, Simulator and Splitter. The Debugger has many features that greatly reduce debugging time.