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General Information

Model 9192 Rackmount High-Speed System Synchronizer Unit synchronizes multiple Pentek Cobalt or Onyx modules within a system. It enables synchronous sampling and timing for a wide range of multichannel high-speed data acquisition, DSP, and software radio applications.

Up to twelve boards can be synchronized using the 9192, each receiving a common clock along with timing signals that can be used for synchronizing, triggering and gating functions.

Input Signals

Model 9192 provides four rear panel SMA connectors to accept input signals from external sources: two for clock, one for gate or trigger and one for a synchronization signal. Clock signals can be applied from an external source such as a high performance sine-wave generator. Gate/trigger and sync signals can come from an external system source. In addition to the SMA connector, a reference clock can be accepted through the first rear panel μ Sync output connector, allowing a single Cobalt or Onyx board to generate the clock for all subsequent boards in the system.

Output Signals

The 9192 provides four rear panel μ Sync output connectors, compatible with a range of high-speed Pentek Cobalt and Onyx boards. The μ Sync signals include a reference clock, gate/trigger and sync signals and are distributed through matched cables, simplifying system design.

Clock Signals

The 9192 can accept a user supplied external clock on its rear panel SMA connector. As an alternative to the external clock, the 9192 can use its on-board programmable voltage controlled crystal oscillator (VCXO) as the clock source. The VCXO can operate alone or be locked to a system reference clock signal delivered to the rear panel reference clock input.

The on-board or external clock can operate at full rate or can be divided and used to register all sync and gate/trigger signals as well as providing a reference clock to all connected boards. In addition, the clock is available at twelve Clock Out SMAs as a sample or reference clock for other boards in the system.

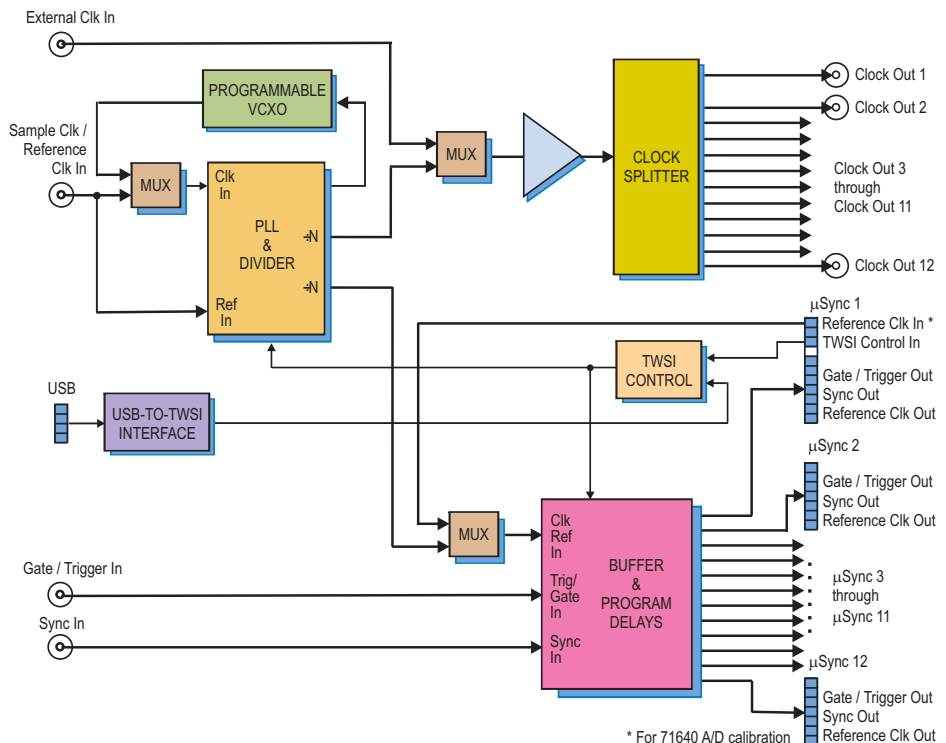
Gate and Synchronization Signals

The 9192 features separate inputs for gate/trigger and sync signals. A programmable delay allows the user to make timing adjustments on the gate/trigger and sync signals before they are sent to buffers for output through the μ Sync output connectors. ➤



Features

- Synchronizes up to twelve separate high-speed Cobalt or Onyx I/O modules
- Synchronizes sampling and data acquisition for multichannel systems
- Synchronizes gating and triggering functions
- Clock rates up to 1.8 GHz
- Rear panel SMA connectors for input signals
- Rear panel μ Sync connectors compatible with a range of Pentek Cobalt and Onyx modules



* For 71640 A/D calibration

► Calibration

The 9192 features twelve calibration outputs specifically designed to work with the 71640 or 71740 3.6 GHz A/D module and provide a signal reference for phase adjustment across multiple D/As

Programming

The 9192 allows programming of operation parameters including: VCXO frequency, clock dividers, and delays that allow the user to make timing adjustments on the gate and sync signals. These adjustments are made before they are sent to buffers for output through the μ Sync connectors.

The 9192 is programmed via a rear panel USB connector or a TWSI control interface on the first μ Sync connector. The control interface is compatible with the front panel μ Sync connectors of all high-speed Cobalt and Onyx modules, thereby providing a single cable connection that carries both control and timing signals.

Supported Products

The 9192 supports all high-speed models in the Cobalt family including the 71630 1 GHz A/D and D/A XMC, the 71640 3.6 GHz A/D XMC and the 71670 Four-channel 1.25 GHz, 16-bit D/A XMC. The 9192 will also support high-speed models in the Onyx family as they become available.

Specifications

Front Panel Sample Clock / Reference Input

Connector Type: SMA

Input Impedance: 50 ohms

Input Level: 0 dBm to +10 dBm, sine wave

Sample Clock Frequency: 100 MHz to 2 GHz

Reference Frequency: 5 to 100 MHz

Rear Panel Gate/Trigger & Sync Inputs

Connector Type: SMA

Input Level: LVTTTL

Rear Panel μ Sync Inputs/Outputs

Quantity: 12

Connector Type: 19-pin μ HDMI

Signal Level: CML

Signals (μ Sync connector 1): Reference Clock In, TWSI control In, Reference Clock Out, Gate/Trigger Out, Sync Out

Signals (μ Sync connectors 2-12): Reference Clock Out, Gate/Trigger Out, Sync Out

Rear Panel Clock / Calibration Outputs

Quantity: 12

Connector Type: SMA

Output Impedance: 50 ohms

Output Level: +6 dBm nominal at 1400 MHz, sine wave

Sample Clock Frequency: 100 MHz to 1.8 GHz

Programmable VCXO:

Frequency Ranges: 10-945 MHz, 970-1134 MHz, and 1213-1417.5 MHz

Tuning Resolution: 32 bits

Unlocked Accuracy: ± 20 ppm

PLL, Divider & Jitter Cleaner

Type: Texas Instruments CDCM7005

Frequency Dividers: 1, 2, 3, 4, 6, 8 and 16

Power: 120VAC

Environmental

Operating Temp: 0° to 50° C

Storage Temp: -20° to 90° C

Relative Humidity: 0 to 95%, non-cond.

Size: Standard 1U Rackmount, 19 in. x 1.75 in.

Ordering Information

Model	Description
9192	Rackmount High-Speed System Synchronizer Unit

Accessories

12 ea. 18" μ Sync cables are supplied; additional cables may be ordered:

2192-018 μ Sync cable - 18"

2192-036 μ Sync cable - 36"

Specifications are subject to change without notice.