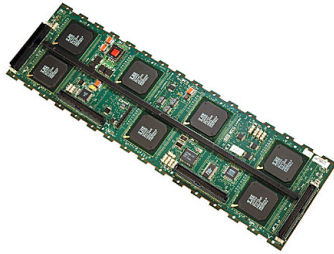


Model 8251

RACE++[®] Series RACEway[™] Interlink Modules



Ordering Information

Model	Description
8251	RACE++ Series RACEway Interlink Module

Options:

-004	ILK4P Module
-008	ILK8P Module*
-012	ILK12P Module*
-016	ILK16P Module*

* Contact Pentek for availability

RACE++ Series RACEway Interlink Modules (ILKP) are circuit board assemblies (designed by Mercury Computer Systems, Inc.) used to connect VME boards to the RACEway fabric. Each ILKP mates with the 64 user-defined 17 mm pins of the VME backplane P2 connector, providing multiple, simultaneous high-speed communication paths between boards.

RACEway is a high-speed synchronous backplane fabric capable of delivering 32-bit word transfers between VME boards at rates up to 267MB/sec (RACE++). The high-speed data channel provided by RACEway is completely independent of the VMEbus, and allows the transfer of data packets up to 2 kB in size.

A significant advantage of RACEway technology is that it provides multiboard communications on a VME system capable of over 2 GB/sec aggregate bandwidth, while leaving front panel connections on VME boards open for other functions.

ILKP modules are available in sizes that bridge 4, 8, 12 and 16 VMEbus slots (ILK4P, ILK8P, ILK12P and ILK16P). In addition, each of these ILKPs contains two expansion connectors for combining with other ILKs for configurations bridging up to 20 slots in a card cage.

RACEway ILKPs are used in conjunction with Pentek's RACE++ Series RACEway ready boards. A 66 MHz clock can be used in a RACE++ system to achieve data transfers of up to 267 MB/sec.

ILKP modules can also be used with RACE 1.0 Series boards, though certain RACEway features are not supported. The maximum clock speed for RACE 1.0 boards is 40 MHz, allowing data transfers of up to 160 MB/sec.

RACE++ and RACE 1.0 boards are compatible, though not all features are supported when combining them in a system. Contact Pentek for further information.